



Byte 2/Bit 6	Corner 3 Locked	high
Byte 2/Bit 7	Corner 4 Locked	high
Byte 3/Bit 0	Corner 1 Landed	high
Byte 3/Bit 1	Corner 2 Landed	high
Byte 3/Bit 2	Corner 3 Landed	high
Byte 3/Bit 3	Corner 4 Landed	high
Byte 3/Bit 4	20FT Positon	high
Byte 3/Bit 5	40FT (In) Positon	high
Byte 3/Bit 6	40FT (Out) Positon	high
Byte 3/Bit 7	45FT Positon	high
Byte 4/Bit 0	Seaside Left Flipper Up	high
Byte 4/Bit 1	Seaside Right Flipper Up	high
Byte 4/Bit 2	Landside Right Flipper Up	high
Byte 4/Bit 3	Landside Left Flipper Up	high
Byte 4/Bit 4	TTDS Seaside	high
Byte 4/Bit 5	TTDS Landside	high
Byte 4/Bit 6	Spare	high
Byte 4/Bit 7	Spare	high
Byte 5/Bit 0	Spreaders Pump ON	high
Byte 5/Bit 1	Spare	high
Byte 5/Bit 2	Spare	high
Byte 5/Bit 3	Spare	high
Byte 5/Bit 4	Spare	high
Byte 5/Bit 5	Spare	high
Byte 5/Bit 6	Spare	high
Byte 5/Bit 7	Pulse Train from Crane (DP COMM. OK)	Alternating
Byte 6 – Byte 15	Spare	high

### Spreader PLC input signals:

Spreader (Slave) Address	Term	Please fill out whether an impulse or a continuous signal is expected	
Byte 0/Bit 0	Lock All Twistlocks Command	Impulse	
Byte 0/Bit 1	Unlock All Twistlocks Command	Impulse	
Byte 0/Bit 2	Retract Command	Impulse	
Byte 0/Bit 3	Spare	Impulse	
Byte 0/Bit 4	Extend Command	Impulse	
Byte 0/Bit 5	TTDS Bypass Command	Continuous	
Byte 0/Bit 6	Spare	Impulse	
Byte 0/Bit 7	Spare	Impulse	
Byte 1/Bit 0	Spare	Impulse	
Byte 1/Bit 1	Spare	Impulse	
Byte 1/Bit 2	Spare	Impulse	
Byte 1/Bit 3	Spare	Impulse	
Byte 1/Bit 4	Seaside Left Flipper Down Command	<del>Impulse</del>	Continuous
Byte 1/Bit 5	Seaside Right Flipper Down Command	<del>Impulse</del>	Continuous
Byte 1/Bit 6	Landside Right Flipper Down Command	<del>Impulse</del>	Continuous
Byte 1/Bit 7	Landside Left Flipper Down Command	<del>Impulse</del>	Continuous
Byte 2/Bit 0	Spare	Impulse	
Byte 2/Bit 1	Spare	Impulse	
Byte 2/Bit 2	Spare	Impulse	
Byte 2/Bit 3	Spare	Impulse	
Byte 2/Bit 4	Spare	Impulse	
Byte 2/Bit 5	Spare	Impulse	
Byte 2/Bit 6	Spare	Impulse	
Byte 2/Bit 7	Pulse Train Return To Crane (DP COMM. OK)	Alternating	
Byte 3 – Byte 15	Spare	Impulse	

Output byte 1/bits 5 & 6 had to be swapped on crane 3

Output byte 1/bits 6 & 7 had to be swapped on crane 4